## DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled:

## WING GATE TRANSISTOR FOR INTEGRATED CIRCUITS

the specification of w	hich				
is attached hereto					
was filed on a	as Application	Serial No and was amend	ed on (	if applicable)	١.
I hereby state that I specification, including	have reviewng the claims,	ed and understand the conte as amended by any amendmer	ents of the nt referred t	above-ident o above.	ified
the same was not in prior to this applicati	nvention there oublic use or o on. I acknowl	the same was ever known or of or more than one year prion sale in the United States of edge the duty to disclose informaccordance with title 37, Contact the same was ever known or same and the same was ever known or same accordance with title 37, Contact the same was ever known or same was ever kn	r to this app America mormation w	olication, and ore than one hich is known	that year
365(a) of any PCT in the United States, lis	any foreign ap ternational ap sted below and	fits under Title 35, United State oplication(s) for patent or investigation which designated at d have also identified belowing a filing date before that	entor's certi least one c any foreign	ficate, or Secondary other	tion than
Prior Foreign Applie	cation(s):				
Number	Country	Day/Month/Year filed	Priority Yes	Claimed No	
I hereby claim the ber application(s) listed b	nefit under 35 lelow:	USC 119(e) of any United Sta	tes provisio	onal	
<b>Prior Provisional Ap</b>	plication(s):				
Application Number	<u>Filin</u>	g Date			

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

**Prior U.S. Application(s):** 

Serial No.

Filing Date

Status: Patented, Pending, Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby revoke any previous Powers of Attorney and appoint the following attorney(s) and/or agent(s), each said individual being a member or associate of The Law Offices of Mikio Ishimaru or being employed by Chartered Semiconductor Manufacturing Ltd.:

Mikio Ishimaru

Reg. No. 27,449

arb

William D. Zahrt II

Reg. No. 26,070

for so long as they remain with such law offices or company with full power of substitution and revocation, to prosecute this application and to transact in connection therewith all business in the Patent and Trademark Office and before competent International Authorities; said appointment to be to the exclusion of myself and my other attorney(s); and all future correspondence should be addressed to:

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